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7/28/04  
JB

Our Docket No.: 42390P9878

Utility Patent Application

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Patent Application of:

Khare, et al.

Serial No.: 09/752,534

Assignee: Intel Corporation

Filed: December 29, 2000

For: A MECHANISM FOR EFFICIENTLY  
SUPPORTING THE FULL MESI (MODIFIED,  
EXCLUSIVE, SHARED, INVALID) PROTOCOL  
IN A CACHE COHERENT MULTI-NODE  
SHARED MEMORY SYSTEM

Examiner: Patel, Hetul B.

Art Unit: 2186

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I hereby certify that I am causing the above-referenced correspondence to be delivered to the USPTO via facsimile transmission to (703) 872-9306 and that this paper or fee has been addressed to the Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit: July 26, 2004Name of Person Mailing Correspondence: Libby H. Hope  
SignatureJuly 26, 2004  
Date

Docket No.: 42390P9878

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Utility Patent Application

Application No.: 09/752,534

**INTRODUCTORY COMMENTS**

Sir:

This Submission is being provided accordance with 37 CFR §1.114(c). A Request for Continued Examination, and fee set forth in 37 CFR 1.17(e) accompany this Submission. Applicants respectfully request reconsideration of the subject application as amended, and in accordance with the following Remarks.